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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Jack Hwang et al.

§ Art Unit: 2822

Serial No.: 10/644,417

§ Examiner: Mark V. Prenty

Filed: August 20, 2003

§ Docket: ITL.0764D1US
P14416DFor: Forming Strained Source Drain
Junction Field Effect Transistors

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO PAPER NO. 20040421

Sir:

In response to the office action mailed April 28, 2004, please amend the above-referenced patent application as follows:

Date of Deposit: July 12, 2004
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden
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